* 1. The problem at hand is to find the parameters based on the following equality:

In the following we assume the interval . Since , must equal , and thus . Since , as well, and hence and therefore also . Again, since , , and hence by squaring both sides we have and from this .

* 1. Along the same lines as in exercise 1.1

In order for , and therefore . For this value of , as well, and hence . From this it follows that . Since and given that , we have that and therefore .

* 1. As in both previous exercises, we set

With it follows that . Since , it must be the case that . It must therefore be the case that . With the values of and cannot be established from the off-diagonal formulas. For that reason, the gate *Z* can have many parametrizations, including when .

* 1. Again, as above

With , . . Hence, and . From these equations we obtain the values and .

* 1. As above,

With , . Then . This means that . Again, there are multiple parametrizations that achieve this, e.g., .

* 1. The gate differs from the *S* gate in the sign of the complex number *i* only. Therefore, the parameters are as follows: and .
	2. The gate *T* differs from the *S* only by having the value instead of *i*. Therefore, the parameter , while for the remaining two parameters there are multiple values for which holds that . To obtain the parameters we use trigonometric identities

This corresponds to

Thus we have .

* 1. The reasoning regarding the gate is similar to that for the *T* gate. Instead of , the has its conjugate . The parameter . Using trigonometric identities

From and we have that . Therefore, any combination of values for and that conforms to the condition is a valid parametrization.

* 1. Finding the parameters for the identity gate is straightforward. From it follows that . Then, from it follows that and therefore .
	2. For the gate

, as above. Then, from it follows that .

* 1. For the gate we have

we have that , and therefore .

**Solutions to chapter 2**

2.1 To obtain the required operation, 3 quantum bits and 3 classical bits are needed. In the following circuit, *q[0]* represents the *x* operand, *q[1]* represents the *y* operand, while *q[2]* is the additional bit in the *OR* gate introduced in the material of this chapter. First, set the two operands into a superposition by applying to each a Hadamard gate *H*. As the *OR* gate requires that the auxialiary quantum bit be equal 1, apply the *NOT* gate to it. The *OR* gate would require a total of 4 *NOT* gates. It can however be simplified due to the fact that one of the operands, namely *x*, is negated. The equivalence of the two subcircuits can be easily checked by writing down truth tables for each of them. Note that the output is presented in the order *c[2], c[1], c[0]*.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[3];
5. h q[0];
6. h q[1];
7. x q[1];
8. x q[2];
9. ccx q[0],q[1],q[2];
10. x q[1];
11. measure q[0] -> c[0];
12. measure q[1] -> c[1];
13. measure q[2] -> c[2];
 |

The output:



2.2 To obtain the required operation, 3 quantum bits and 3 classical bits are needed. *q[0]* represents the *x* operand, *q[1]* represents *y*, while *q[2]* is the additional bit in the *OR* gate introduced in the material of this chapter. First, set the two operands into a superposition by applying to each a Hadamard gate *H*. As the *OR* gate requires that the auxialiary quantum bit be equal 1, apply the Pauli *X* gate to it. The *OR* gate would require a total of 4 *X* gates. It can however be simplified due to the fact that one of the operands, namely *y*, is negated. Note that the output is presented in the order *c[2], c[1], c[0]*.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[3];
5. h q[0];
6. h q[1];
7. x q[2];
8. x q[0];
9. ccx q[0],q[1],q[2];
10. x q[0];
11. measure q[0] -> c[0];
12. measure q[1] -> c[1];
13. measure q[2] -> c[2];];
14. measure q[0] -> c[0];
15. measure q[1] -> c[1];
16. measure q[2] -> c[2];
 |

The output:



2.3 A possible implementation of this operation could be as below. To this end, 5 quantum bits and 3 classical bits are required. Observe that . Therefore, in the following, we separate the operations into three parts: , , and the *OR* operation on the two. The three operations are separated by barriers (barriers are only used for convenience and they do not contribute to the meaning of the circuit). Everything up to the first barrier, except the two Hadamard gates, encodes the clause . Clause is encoded between the first and the second barrier. The *OR* operation on the two clauses is encoded between the second and the third barrier. In the following circuit, *q[0]* represents *x*, *q[1]* represents *y*, *q[2]* is the auxialiary quantum bit for the *AND* operation in the term , *q[3]* is the auxiliary bit for the *AND* operation in the term , while *q[4]* is the auxiliary bit for the *OR* operation mentioned above. Note that the output is presented in the order *c[2], c[1], c[0]*.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[3];
5. h q[0];
6. h q[1];
7. x q[1];
8. ccx q[0],q[1],q[2];
9. x q[1];
10. barrier q[0],q[1],q[2],q[3],q[4];
11. x q[0];
12. x q[1];
13. ccx q[0],q[1],q[3];
14. x q[0];
15. x q[1];
16. barrier q[0],q[1],q[2],q[3],q[4];
17. x q[2];
18. x q[3];
19. x q[4];
20. ccx q[2],q[3],q[4];
21. x q[2];
22. x q[3];
23. barrier q[0],q[1],q[2],q[3],q[4];
24. measure q[0] -> c[0];
25. measure q[1] -> c[1];
26. measure q[4] -> c[2];
 |

The output:



2.4 A possible implementation of this operation could be as below. To this end, 5 quantum bits and 3 classical bits are required. The separation of operations is along the same lines as in the previous exercise. Observe that . Therefore, in the following, we separate the operations into three parts: , , and the *OR* operation on the two. The three operations are separated by barriers (barriers are only used for convenience and they do not contribute to the meaning of the circuit). Everything up to the first barrier, except the two Hadamard gates, encodes the clause . Clause is encoded between the first and the second barrier. The *OR* operation on the two clauses is encoded between the second and the third barrier. *q[0]* represents *x*, *q[1]* represents *y*, *q[2]* is the auxialiary quantum bit for the *AND* operation in the term , *q[3]* is the auxiliary bit for the *AND* operation in the term , while *q[4]* is the auxiliary bit for the *OR* operation mentioned above. Note that the output is presented in the order *c[2], c[1], c[0]*.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[3];
5. h q[0];
6. h q[1];
7. x q[0];
8. ccx q[0],q[1],q[2];
9. x q[0];
10. barrier q[0],q[1],q[2],q[3],q[4];
11. x q[0];
12. x q[1];
13. ccx q[0],q[1],q[3];
14. x q[0];
15. x q[1];
16. barrier q[0],q[1],q[2],q[3],q[4];
17. x q[2];
18. x q[3];
19. x q[4];
20. ccx q[2],q[3],q[4];
21. x q[2];
22. x q[3];
23. barrier q[0],q[1],q[2],q[3],q[4];
24. measure q[0] -> c[0];
25. measure q[1] -> c[1];
26. measure q[4] -> c[2];
 |

The output:



2.5 To obtain the required operation, 3 quantum bits and 3 classical bits are needed. *q[0]* represents the *x* operand, *q[1]* represents *y*, while *q[2]* is the additional bit in the *AND* gate introduced in the material of this chapter. First, set the two operands into a superposition by applying to each a Hadamard gate *H*. The two *X* gates are to account for the fact that operand *x* is negated. Note that the output is presented in the order *c[2], c[1], c[0]*.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[3];
5. h q[0];
6. h q[1];
7. x q[0];
8. ccx q[0],q[1],q[2];
9. x q[0];
10. measure q[0] -> c[0];
11. measure q[1] -> c[1];
12. measure q[2] -> c[2];
 |

The output:



2.6 To obtain the required operation, 3 quantum bits and 3 classical bits are needed. *q[0]* represents the *x* operand, *q[1]* represents *y*, while *q[2]* is the additional bit in the *AND* gate introduced in the material of this chapter. First, set the two operands into a superposition by applying to each a Hadamard gate *H*. The two *X* gates are to account for the fact that operand *y* is negated. Note that the output is presented in the order *c[2], c[1], c[0]*.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[3];
5. h q[0];
6. h q[1];
7. x q[1];
8. ccx q[0],q[1],q[2];
9. x q[1];
10. measure q[0] -> c[0];
11. measure q[1] -> c[1];
12. measure q[2] -> c[2];
 |

The output:



**Solutions to chapter 3**

3.1 The circuit for the satisfiability problem specified by the oracle is very similar to the one given for in this chapter (see also the answer to exercise 3.4). The difference is that the input is negated (that is *q[3]*). *q[0]* is a workspace qubit, *q[1]* is not used, *q[2]* represents , *q[3]* is for , while *q[4]* is for . We introduce barriers to separate different parts of the code. Everything up to the first barrier represents the initial encoding. *q[0]* represents the state , as in the material, while *q[3]* and *q[4]* are set into a uniform superposition. The oracle that negates the amplitude of the qubitfor which the condition is fulfilled is located between the first and the second barrier. To represent the fact that the value of is negated, the *NOT* gate is used on the qubit representing . After the oracle has completed its task, another *NOT* gate is applied to to uncompute. The circuit part between the second and the third barriers has been introduced in the material of this chapter and encodes the Grover diffusion operator.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[2];
5. x q[0];
6. h q[3];
7. h q[4];
8. h q[0];
9. barrier q[0],q[1],q[2],q[3],q[4];
10. x q[3];
11. ccx q[4],q[3],q[2];
12. cx q[2],q[0];
13. ccx q[4],q[3],q[2];
14. x q[3];
15. barrier q[0],q[1],q[2],q[3],q[4];
16. h q[3];
17. h q[4];
18. x q[3];
19. x q[4];
20. h q[4];
21. cx q[3],q[4];
22. x q[3];
23. h q[4];
24. u3(2\*pi,0,0) q[3];
25. x q[4];
26. h q[3];
27. h q[4];
28. barrier q[0],q[1],q[2],q[3],q[4];
29. measure q[4] -> c[0];
30. measure q[3] -> c[1];
 |

The output:



3.2 The circuit for the satisfiability problem specified by the oracle is very similar to the one given for in exercise 3.1. The difference is that the input is negated (that is *q[2]*) instead of . *q[0]* is a workspace qubit, *q[1]* is not used, *q[2]* represents , *q[3]* is for , while *q[4]* is for . We introduce barriers to separate different parts of the code. Everything up to the first barrier represents the initial encoding. *q[0]* represents the state , as in the material, while *q[3]* and *q[4]* are set into a uniform superposition. The oracle that negates the amplitude of the qubit for which the condition is fulfilled is located between the first and the second barrier. To represent the fact that the value of is negated, the *NOT* gate is used on the qubit representing . After the oracle has completed its task, another *NOT* gate is applied to to uncompute. The circuit part between the second and the third barriers has been introduced in the material of this chapter and encodes the Grover diffusion operator.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[2];
5. x q[0];
6. h q[3];
7. h q[4];
8. h q[0];
9. barrier q[0],q[1],q[2],q[3],q[4];
10. x q[4];
11. ccx q[4],q[3],q[2];
12. cx q[2],q[0];
13. ccx q[4],q[3],q[2];
14. x q[4];
15. barrier q[0],q[1],q[2],q[3],q[4];
16. h q[3];
17. h q[4];
18. x q[3];
19. x q[4];
20. h q[4];
21. cx q[3],q[4];
22. x q[3];
23. h q[4];
24. u3(2\*pi,0,0) q[3];
25. x q[4];
26. h q[3];
27. h q[4];
28. barrier q[0],q[1],q[2],q[3],q[4];
29. measure q[4] -> c[0];
30. measure q[3] -> c[1];
 |

The output:



3.3 In theory, the circuit for the satisfiability problem specified by the oracle should be very similar to the one given for in this chapter (see also the answer to exercise 3.4). The difference would be that the quantum bit *q[2]* is negated in order to represent the *NAND* gate. *q[0]* is a workspace qubit, *q[1]* is not used, *q[2]* represents , *q[3]* is for , while *q[4]* is for . We introduce barriers to separate different parts of the code. Everything up to the first barrier represents the initial encoding. *q[0]* represents the state , as in the material, while *q[3]* and *q[4]* are set into a uniform superposition. The oracle that negates the amplitude of the qubitfor which the condition is fulfilled is located between the first and the second barrier. As mentioned above, the *NOT* gate is used on the qubit representing . After the oracle has completed its task, another *NOT* gate is applied to to uncompute. The circuit part between the second and the third barriers has been introduced in the material of this chapter and encodes the Grover diffusion operator. After executing the code we notice that the obtained output is incorrect. In fact it shows the state with probability 1. The correct answer should have included equal probabilities for three states: , , and . These three states are the correct outcomes for the *NAND* gate. The reason we obtained the wrong answer is that Grover’s algorithm imposes a limitation on the ratio between the number of answers (A) and the number of all opossible outcomes (O). This limitation is . In this exercise, this ratio is however.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[2];
5. x q[0];
6. h q[3];
7. h q[4];
8. h q[0];
9. barrier q[0],q[1],q[2],q[3],q[4];
10. x q[2];
11. ccx q[4],q[3],q[2];
12. cx q[2],q[0];
13. ccx q[4],q[3],q[2];
14. x q[2];
15. barrier q[0],q[1],q[2],q[3],q[4];
16. h q[3];
17. h q[4];
18. x q[3];
19. x q[4];
20. h q[4];
21. cx q[3],q[4];
22. x q[3];
23. h q[4];
24. u3(0,0,0) q[3];
25. x q[4];
26. h q[3];
27. h q[4];
28. barrier q[0],q[1],q[2],q[3],q[4];
29. measure q[4] -> c[0];
30. measure q[3] -> c[1];
 |

The output:



3.4 The circuit for the satisfiability problem specified by the oracle which was given in the material of this chapter, can be simplified by replacing the twelve gates used to represent the *CCNOT* operation with the *CCNOT* gate itself (in code: *ccx*). As the procedure of deriving the circuit has been described in detail in the material, here we only present the corresponding circuit, the code and its output. As before, *q[0]* is a workspace qubit, *q[1]* is not used, *q[2]* represents , *q[3]* is for , while *q[4]* is for . We introduce barriers to separate different parts of the code. Everything up to the first barrier represents the initial encoding. *q[0]* represents the state , as in the material, while *q[3]* and *q[4]* are set into a uniform superposition. The oracle that negates the amplitude of the qubit for which the condition is fulfilled is located between the first and the second barrier. The circuit part between the second and the third barriers has been introduced in the material of this chapter and encodes the Grover diffusion operator.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[5];
4. creg c[2];
5. x q[0];
6. h q[3];
7. h q[4];
8. h q[0];
9. barrier q[0],q[1],q[2],q[3],q[4];
10. ccx q[4],q[3],q[2];
11. cx q[2],q[0];
12. ccx q[4],q[3],q[2];
13. barrier q[0],q[1],q[2],q[3],q[4];
14. h q[3];
15. h q[4];
16. x q[3];
17. x q[4];
18. h q[4];
19. cx q[3],q[4];
20. x q[3];
21. h q[4];
22. u3(2\*pi,0,0) q[3];
23. x q[4];
24. h q[3];
25. h q[4];
26. barrier q[0],q[1],q[2],q[3],q[4];
27. measure q[4] -> c[1];
28. measure q[3] -> c[0];
 |

The output:



**Solutions to chapter 4**

4.1 The mapping this oracle performs is . The following table shows the particular results. Input 0/1 indicates that the respective value is either 0 or 1.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  |  |  |  |  |
| 0/1 | 0 | 0 | 0 |  |
| 0/1 | 0 | 0 | 1 |  |
| 0/1 | 0 | 1 | 0 |  |
| 0/1 | 0 | 1 | 1 |  |
| 0/1 | 1 | 0 | 0 |  |
| 0/1 | 1 | 0 | 1 |  |
| 0/1 | 1 | 1 | 0 |  |
| 0/1 | 1 | 1 | 1 |  |

This corresponds to the sequence of rotations . As these values (see the table above) appear twice, the expected frequency is 2. With this the period is .

The solution follows closely that already introduced in the material of this chapter. Quantum bits *q[0], q[1], q[2],* and *q[3]* correspond to , respectively. The first column indicates that all 4 quantum bits are set into a uniform superposition by applying to them the Hadamard gate. In the next column, the respective rotations are encoded. In accordance with the mapping formula, quantum bit *q[1]* is rotated by , *q[2]* by , and *q[3]* by . These operations are delineated by the barrier. After the barrier, the inverse quantum Fourier transform is applied. This transformation is the same (starting in row 13 of the code) as introduced in the chapter material. What changes is the application of the *U1* gates to the qubits *q[1], q[2], q[3]* corresponding to , respectively.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[4];
4. creg c[4];
5. h q[0];
6. h q[1];
7. h q[2];
8. h q[3];
9. u1(pi) q[1];
10. u1(pi/2) q[2];
11. u1(pi/4) q[3];
12. barrier q[0],q[1],q[2],q[3];
13. h q[0];
14. cu1(-pi/2) q[1],q[0];
15. cu1(-pi/4) q[2],q[0];
16. cu1(-pi/8) q[3],q[0];
17. h q[1];
18. cu1(-pi/2) q[2],q[1];
19. cu1(-pi/4) q[3],q[1];
20. h q[2];
21. cu1(-pi/2) q[3],q[2];
22. h q[3];
23. swap q[0],q[3];
24. swap q[1],q[2];
25. measure q[0] -> c[3];
26. measure q[1] -> c[2];
27. measure q[2] -> c[1];
28. measure q[3] -> c[0];
 |

The output:



4.2 The mapping this oracle performs is . The following table shows the particular results. Input 0/1 indicates that the respective value is either 0 or 1.

|  |  |  |
| --- | --- | --- |
|  |  |  |
| 00/01/10/11 | 0 |  |
| 00/01/10/11 | 1 |  |

The oracle oscillates between two values corresponding to rotations by. The frequency is 4, whereby the period equals 8/4=2. Our solution follows closely that already introduced in the material of this chapter. Quantum bits *q[0], q[1],* and *q[2]* correspond to , respectively. 3 classical registers are needed to encode the output. The first column indicates that all 3 quantum bits are set into a uniform superposition by applying to them the Hadamard gate. In the next column, the respective rotations are encoded. In accordance with the mapping formula, quantum bit *q[2]* is rotated by . These operations are delineated by the barrier. After the barrier, the inverse quantum Fourier transform is applied. This transformation is the same (starting in row 10 of the code) as in the chapter material, but applied to 3 quantum bits instead of 4. What changes is the application of the *U1* gate to the qubit *q[2]* corresponding to . Moreover, the swapping operation takes place between the most significant digit and the least significant digit only.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[3];
4. creg c[3];
5. h q[0];
6. h q[1];
7. h q[2];
8. u1(pi) q[2];
9. barrier q[0],q[1],q[2];
10. h q[0];
11. cu1(-pi/2) q[1],q[0];
12. cu1(-pi/4) q[2],q[0];
13. h q[1];
14. cu1(-pi/2) q[2],q[1];
15. h q[2];
16. swap q[0],q[2];
17. measure q[0] -> c[2];
18. measure q[1] -> c[1];
19. measure q[2] -> c[0];
 |

The output:



4.3 The mapping this oracle performs is . The following table shows the particular results. Input 0/1 indicates that the respective value is either 0 or 1.

|  |  |  |  |
| --- | --- | --- | --- |
|  |  |  |  |
| 0/1 | 0 | 0 |  |
| 0/1 | 0 | 1 |  |
| 0/1 | 1 | 0 |  |
| 0/1 | 1 | 1 |  |

The oracle oscillates between four values corresponding to rotations by. The frequency is 2, whereby the period equals 8/2=4. Our solution follows closely that already introduced in the material of this chapter. Quantum bits *q[0], q[1],* and *q[2]* correspond to , respectively. 3 classical registers are needed to encode the output. The first column indicates that all 3 quantum bits are set into a uniform superposition by applying to them the Hadamard gate. In the next column, the respective rotations are encoded. In accordance with the mapping formula, quantum bit *q[1]* is rotated by , while *q[2]* is rotated by . These operations are delineated by the barrier. After the barrier, the inverse quantum Fourier transform is applied. This transformation is the same (starting in row 11 of the code) as in the chapter material, but applied to 3 quantum bits instead of 4. What changes is the application of the *U1* gate to the qubits *q[1]* and *q[2]* corresponding to and , respectively. Moreover, the swapping operation takes place between the most significant digit and the least significant digit only.

The circuit:



The code:

|  |
| --- |
| 1. OPENQASM 2.0;
2. include "qelib1.inc";
3. qreg q[3];
4. creg c[3];
5. h q[0];
6. h q[1];
7. h q[2];
8. u1(pi) q[1];
9. u1(pi/2) q[2];
10. barrier q[0],q[1],q[2];
11. h q[0];
12. cu1(-pi/2) q[1],q[0];
13. cu1(-pi/4) q[2],q[0];
14. h q[1];
15. cu1(-pi/2) q[2],q[1];
16. h q[2];
17. swap q[0],q[2];
18. measure q[0] -> c[2];
19. measure q[1] -> c[1];
20. measure q[2] -> c[0];
 |

The output:



**Solutions:**

5.1 Since the remainder , , where *xd* is a multiple of *d*. From this it follows that . Let and let . Then, on the one hand, and , and also . With this, we have that and thus . Thus, . On the other hand, and . Hence, , from which it follows that with . Thus, . Therefore it must hold that .

5.2 Let

Since for each , it follows that as well. By Bézout’s identity there exist two integers and such that . Then a solution can be constructed as follows:

 Each congruence relation is then represented as

 Moreover, any two solutions are equal modulo *Y.* To prove that let and be two solutions to the given system of equations. Since are pairwise coprime, it follows that , , … . Thus, , or, equivalently

5.3 By the definition of binomial coefficients

As binomial coefficients represent integers, is an integer. From the above equation it can be verified that is a multiple of *p.* Hence, what must be shown is that is an integer. To that end we use Euclid’s lemma. Euclid’s lemma states that if a prime *n* divides the product *ab*,where both *a* and *b* are integers, then *n* must divide at least one of them. Since is an integer, it follows that *k!* divides . Moreover, since *k!* does not divide *p* because *p* is prime, then by Euclid’s lemma, *k!* must divide and so is an integer. Therefore, *p* divides .

5.4 This proof is due to James Ivory (“Demonstration of a theorem respecting prime numbers”, *New Series of the Mathematical Depository,* 1(2): 6-8, 1806). Let *a* be an integer and *p* be prime that does not divide *a*. The list of the first *p-1* positive multiples of *a* is

By reducing each element modulo *p*, a new list is obtained that consists of a permutation of the integers . Therefore, multiplying the elements

 This corresponds to

 and further to

 Now let *a* be any integer and *p* be a prime. If then . If *p* does not divide *a* then multiplying the above equation by *a*

 gives the required result .

5.5 Consider the multiplicative group modulo : . Every element in this group has a unique inverse. Let the elements of this group be denoted as . Then for the elements are also element of In analogy to the proof of Fermat’s little theorem, multiplying the elements corresponds then to

 Cancelling equal terms leads to .

5.6 The following proof has been adapted from the proof given in “Continued Fractions, Pell's equation, and other applications" by Jeremy Booher and holds for any rational fraction, not only .

Assume is not a convergent of the continued fraction of . Then *r* can be picked to lie between the denominators of two convergents and of , that is, . Suppose

 The determinant of the matrix in the following equation

is 1 or -1 based on the difference between two successive convergents, which is given by

 Then for the system of equations

holds that . *u* and *v* cannot be both positive or both negative beause this would imply that which contradicts our assumption that .

With (2) we have

Since even convergents are increasing and odd convergents are decreasing with lying in between them, and given that , it must be that either (1) and have the same sign, or (2) one of them is zero. Therefore,

For

to be true, either (1) and , or (2) . Assuming (1) is the case, then and is thus a convergent of . Assuming (2) is the case, then , which contradicts our assumption that .

As opposed to equation (1) suppose that

 Then

 From this it follows that

 However

Since due to (3)

and because we assumed that *z* and *r* are positive integers and therefore the nominator in (4) has to be an integer, we have

which implies that is a convergent of .

5.7

 Let . Then by trigonometric identity formula for double angles we obtain

**Solutions to exercises in chapter 6:**

6.1 Let be the only solution of a given search problem. Then, recalling that the inner product , we have

6.2 For the matrix has dimensions 4x4 and can be derived as follows:

6.3 First, derive

Then applying *U* to we obtain

Thus *U* has reflected vector about as shown in Fig. 6.7.

6.4 The matrix of *G* is

From the state we have that and . From the state we have that while . The elements of Grovers operator can then be obtained from the following triginometric identities

With the above the mtrix has the form

6.5 We need to solve the equation

where is an eigenvector and is its corresponding eigenvalue. To that end we find the characteristic equation

to be

The two eigenvalues are therefore (the complex value is due to the fact that )

and

The corresponding eigenvectors are calculated as follows. For

and hence . Therefore the eigenvector is . For

and hence . This corresponds to the eigenvector .